

FIG. 1

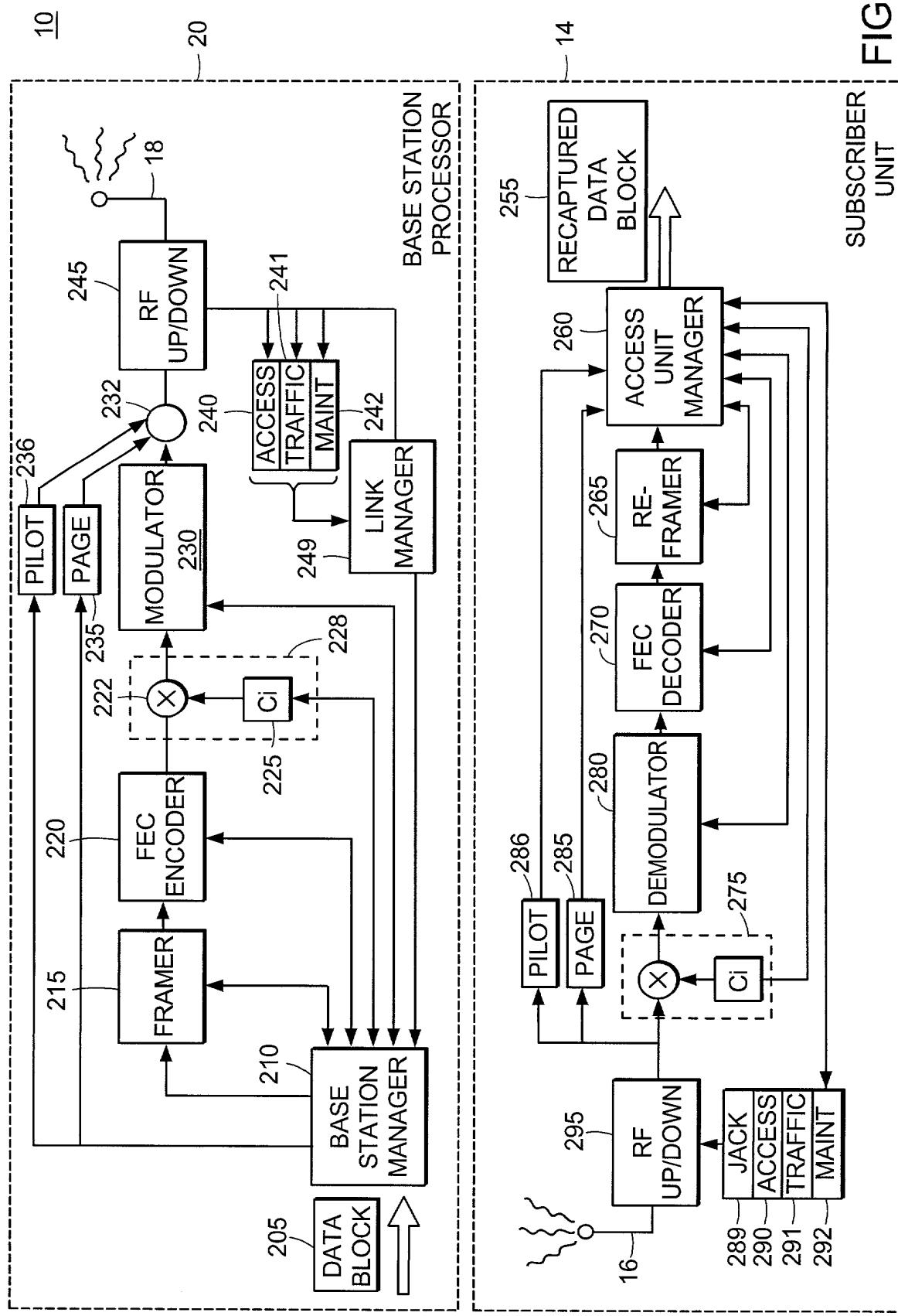


FIG. 2

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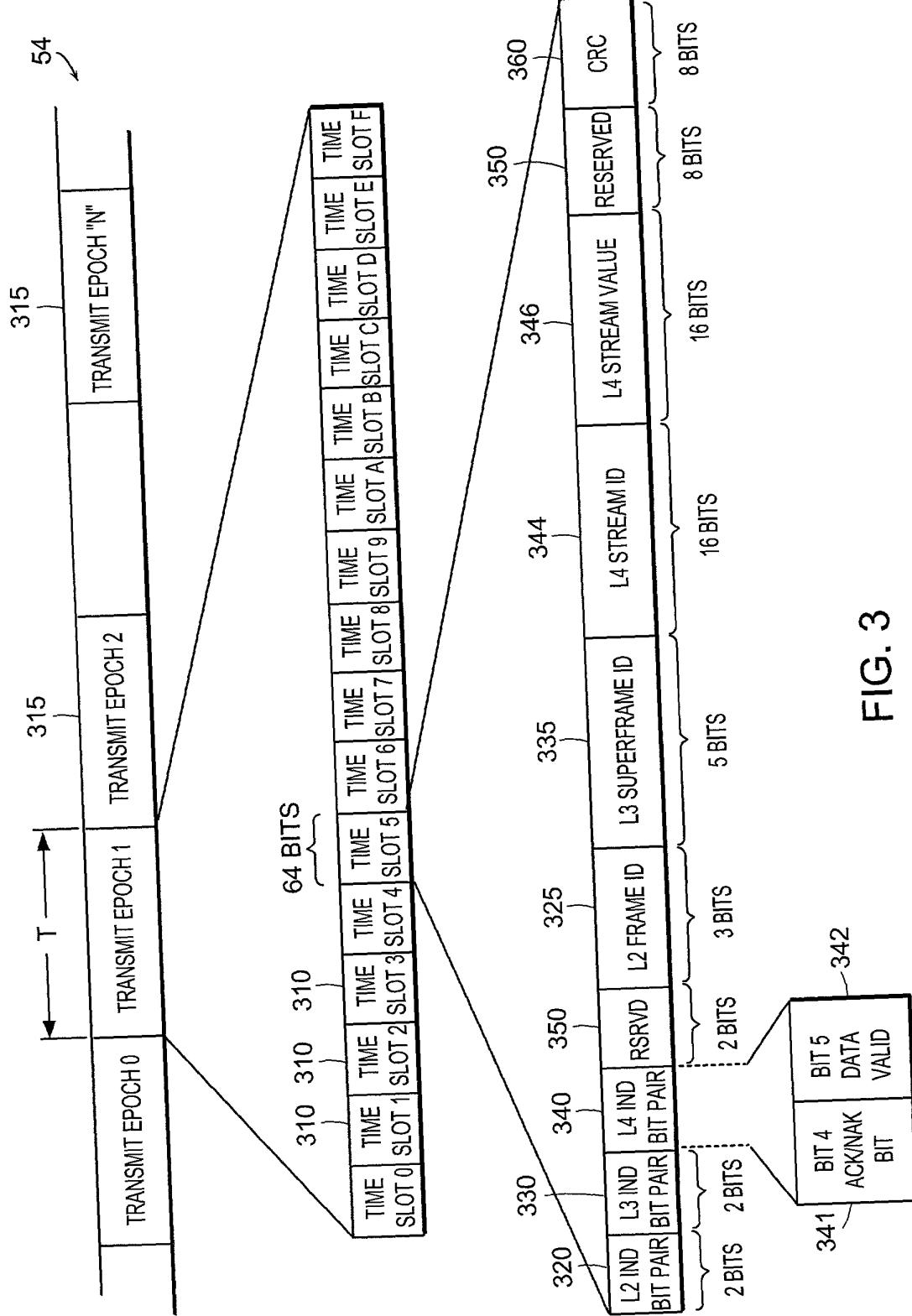
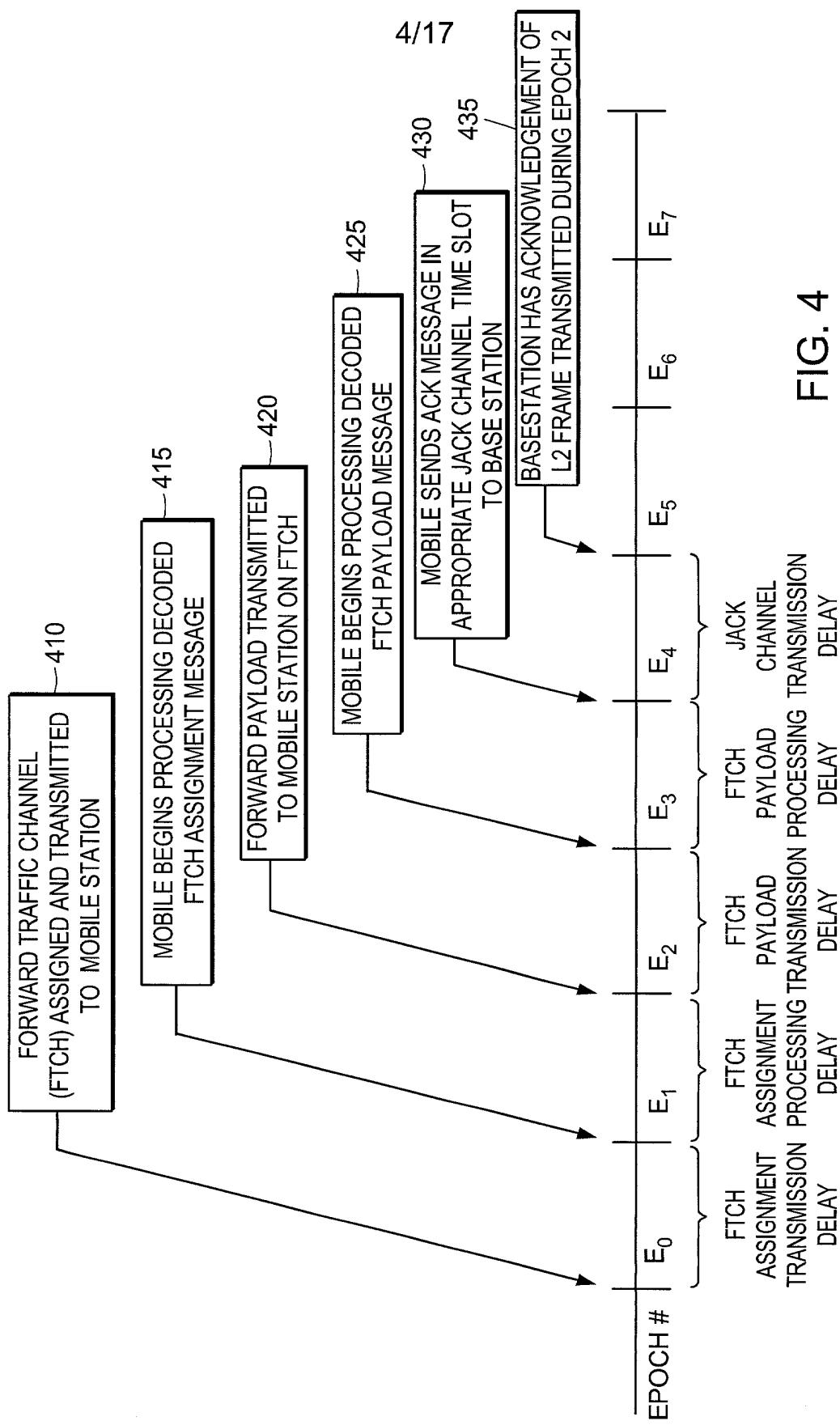


FIG. 3

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EPOCH OFFSET	JACK CHANNEL TIME SLOT #															
0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F	
+0																
+1																
+2																
+3					γ		γ		γ							

500 ↗

510 510 510

FIG. 5

344 ↘

LAYER 4 STREAM ID	32-BIT SOURCE ADDRESS	32-BIT DESTINATION ADDRESS	16-BIT SOURCE PORT	16-BIT DESTINATION PORT
0001	~~~	~~~	~~~	~~~
0002	~~~	~~~	~~~	~~~
0003	~~~	~~~	~~~	~~~
0004	~~~	~~~	~~~	~~~

FIG. 6

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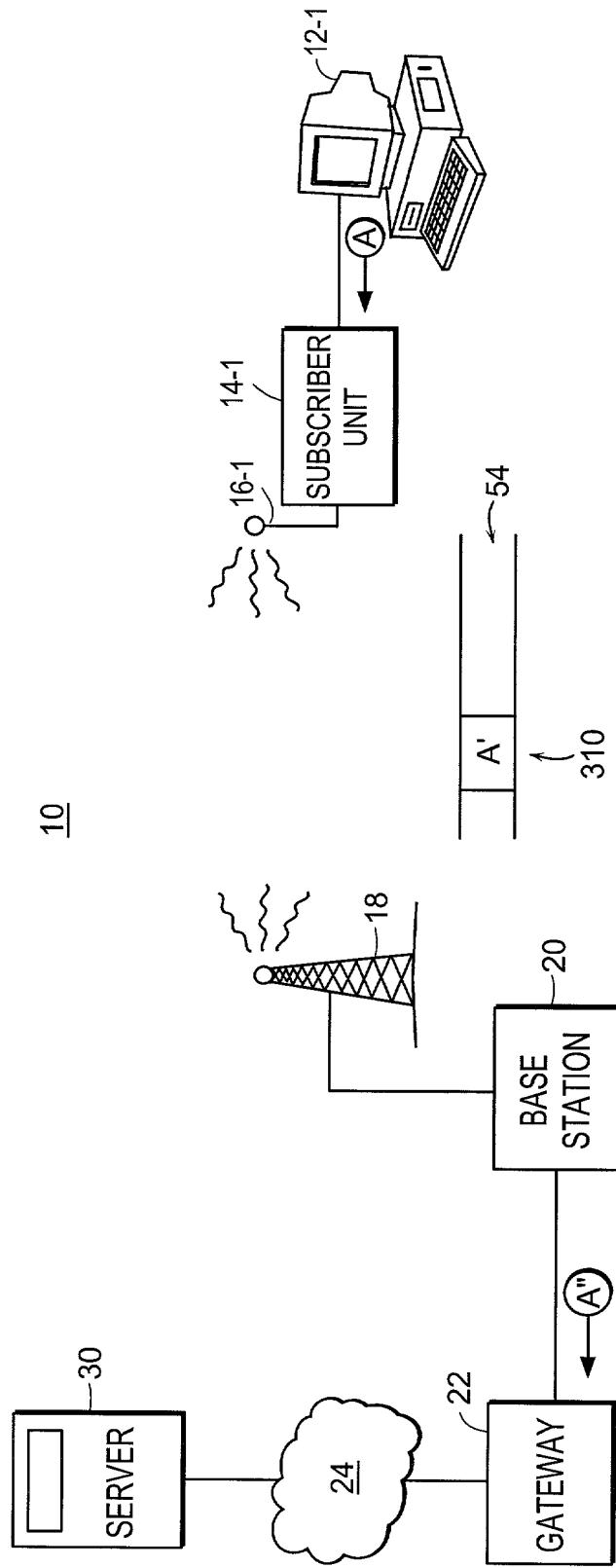


FIG. 7

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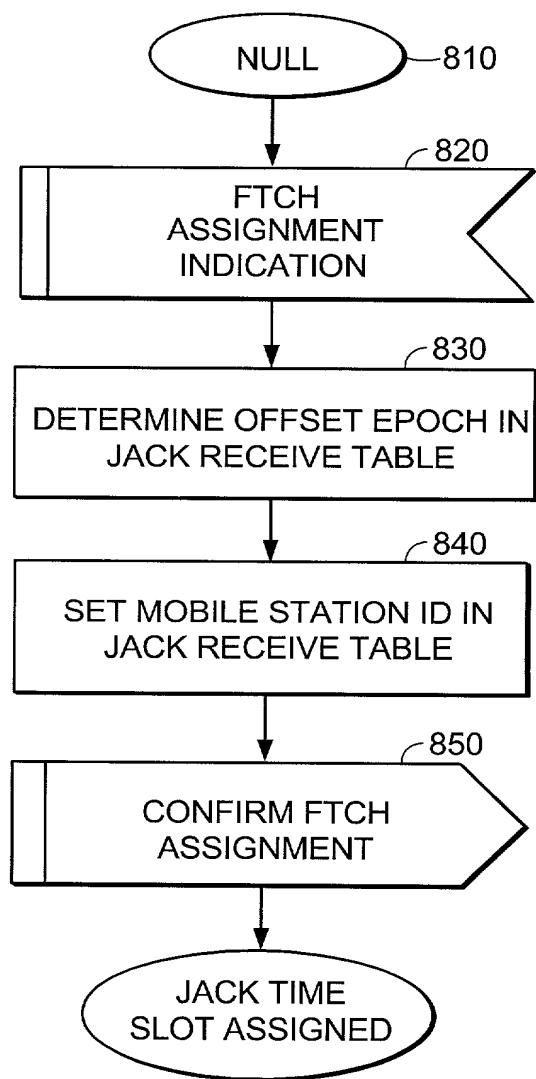


FIG. 8

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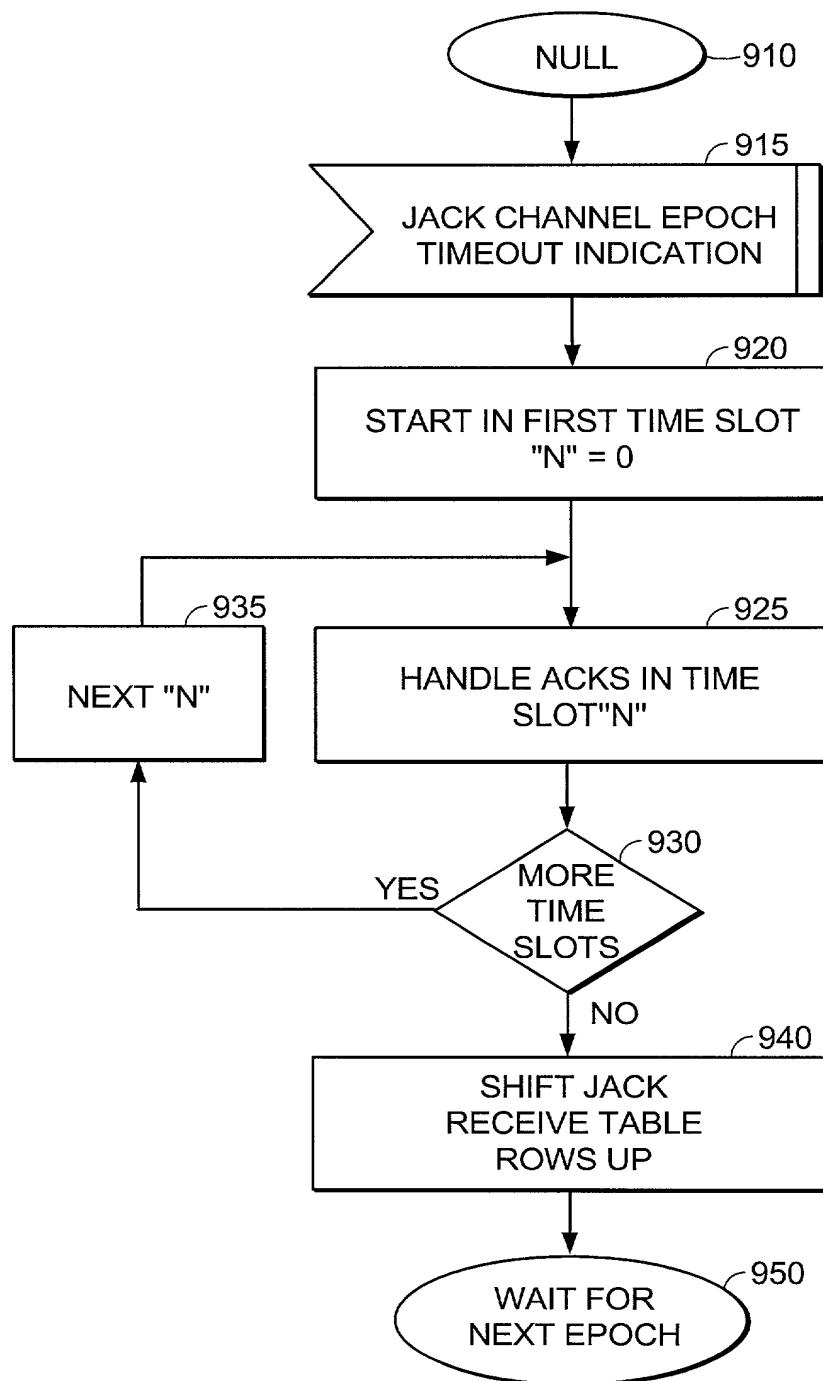


FIG. 9

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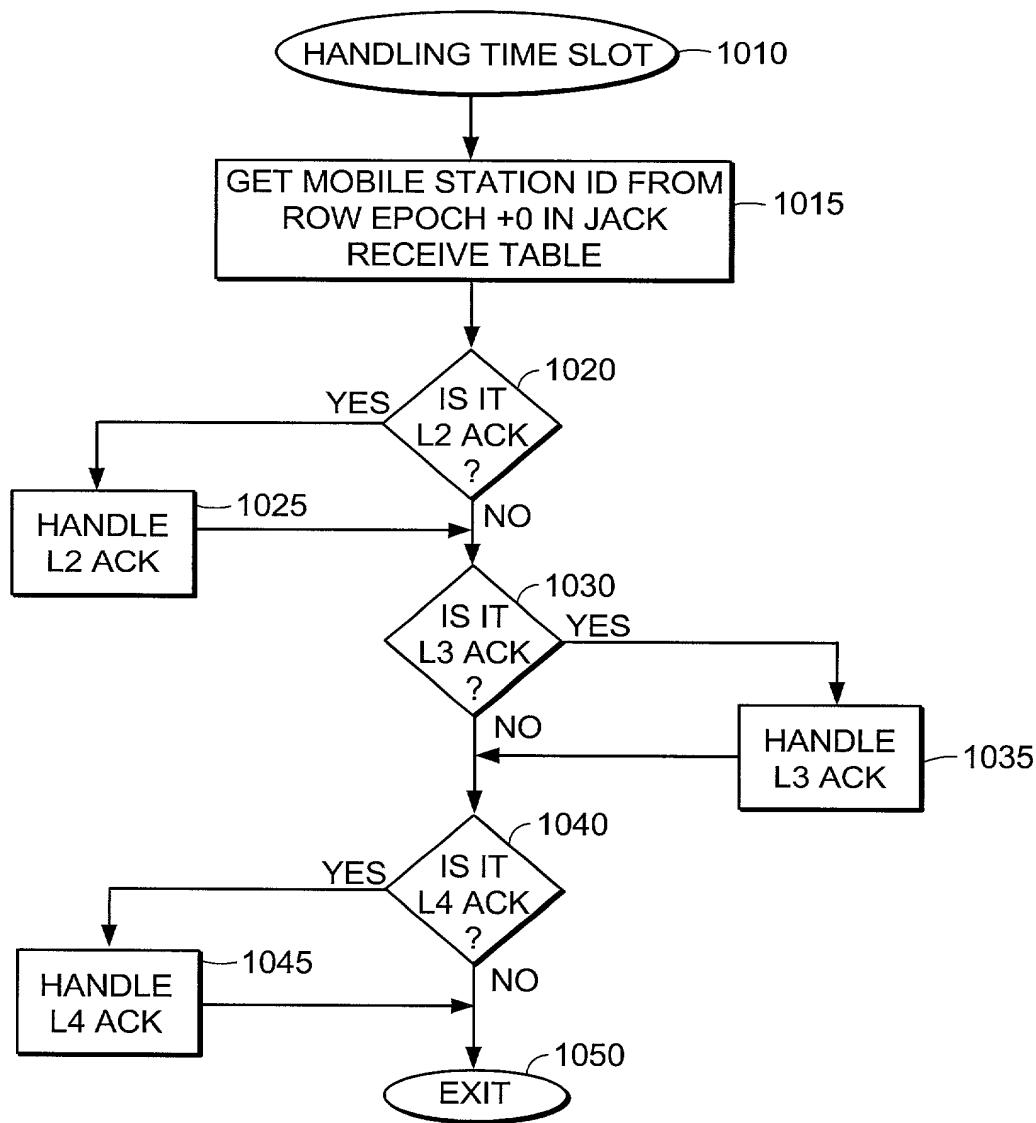
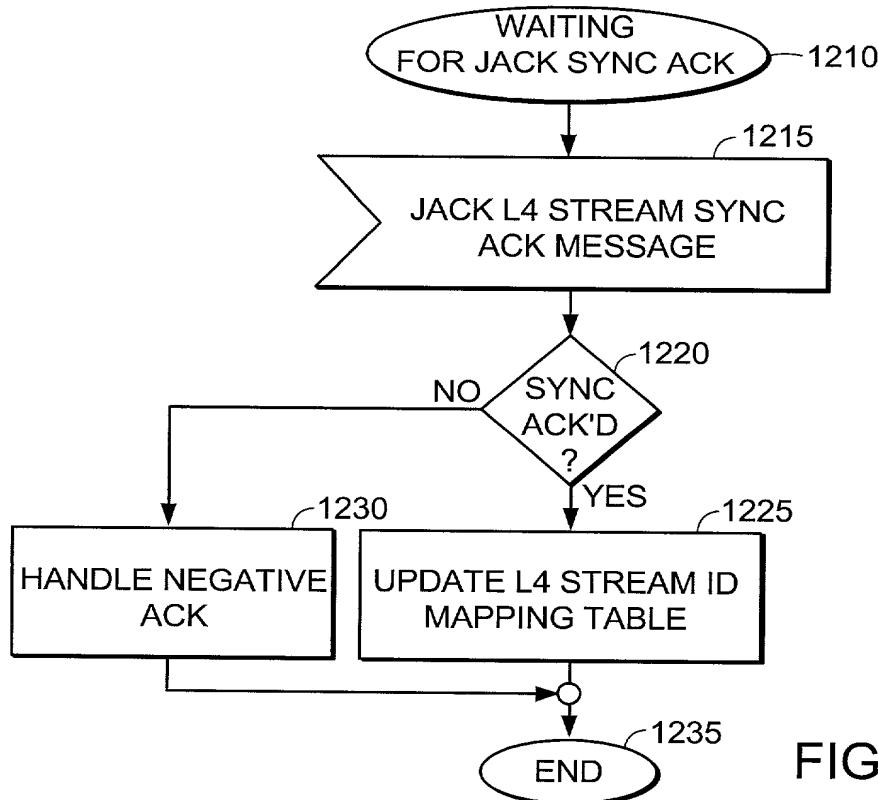
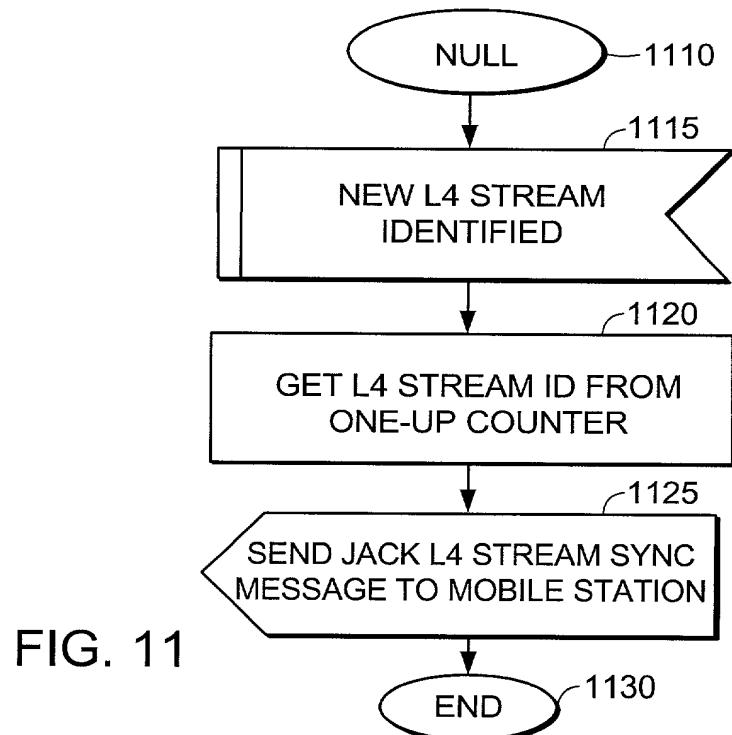


FIG. 10

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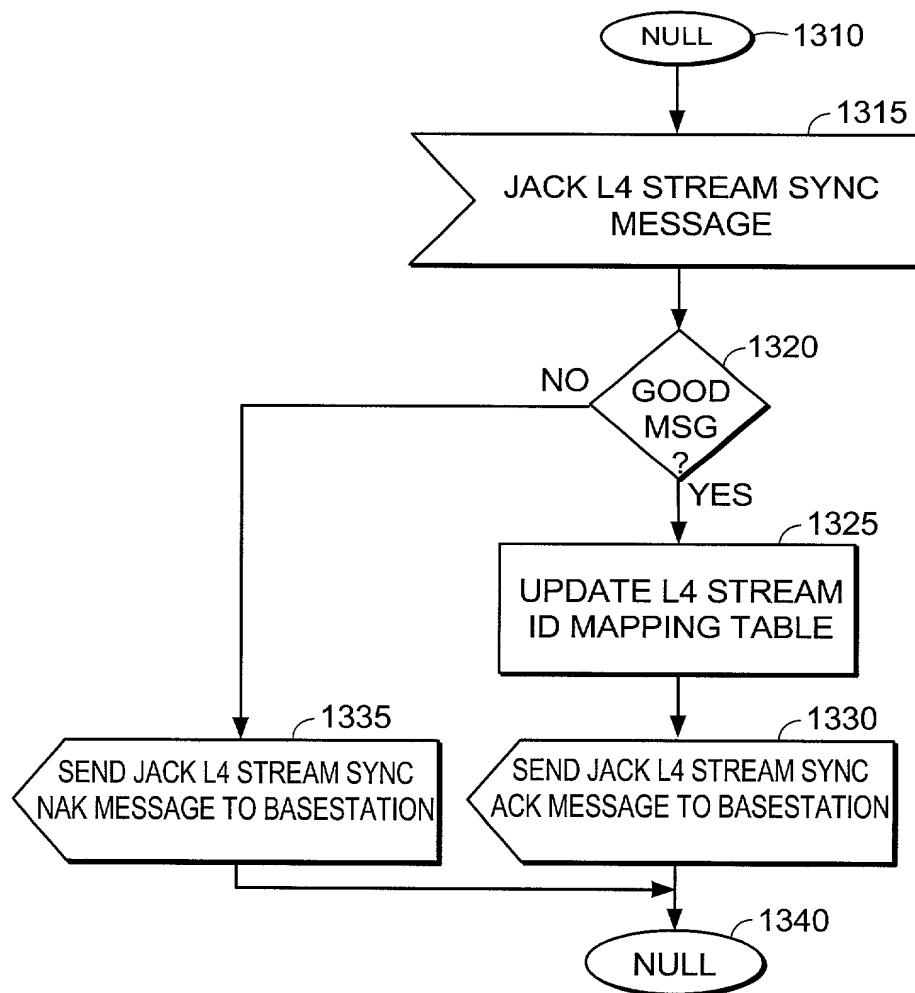


FIG. 13

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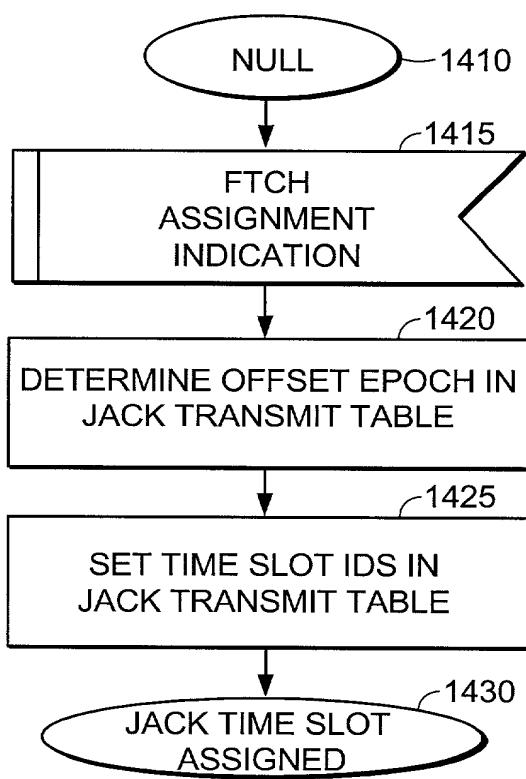


FIG. 14

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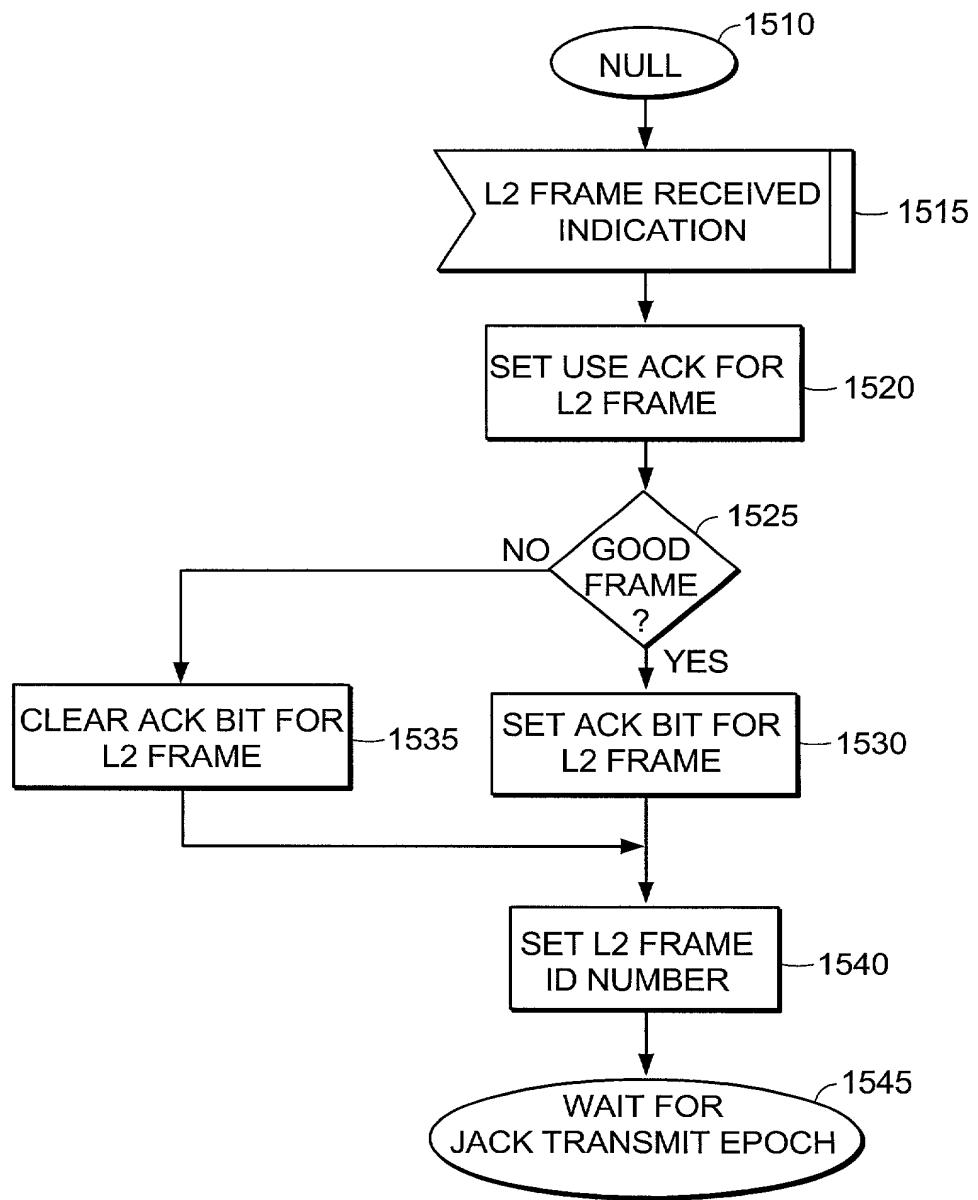


FIG. 15

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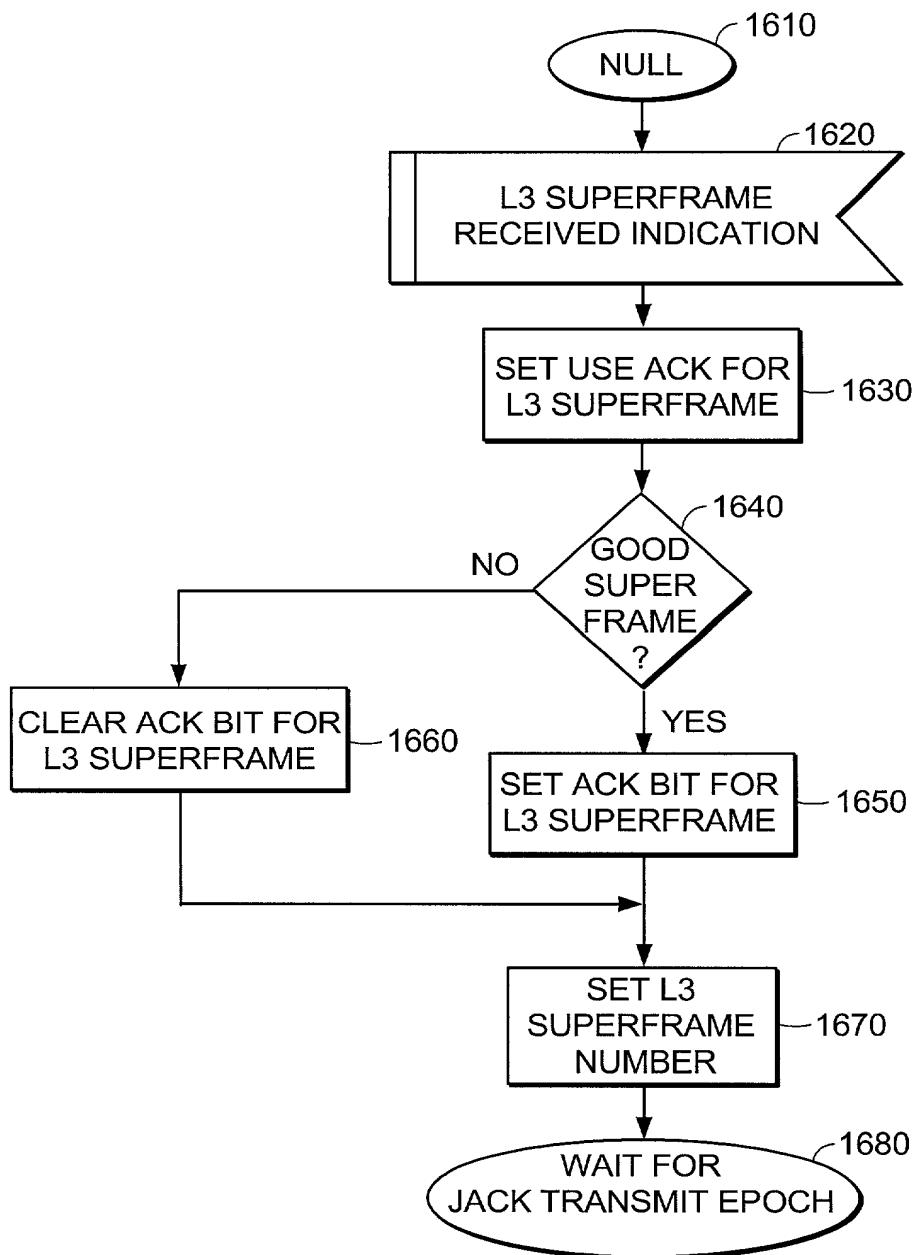


FIG. 16

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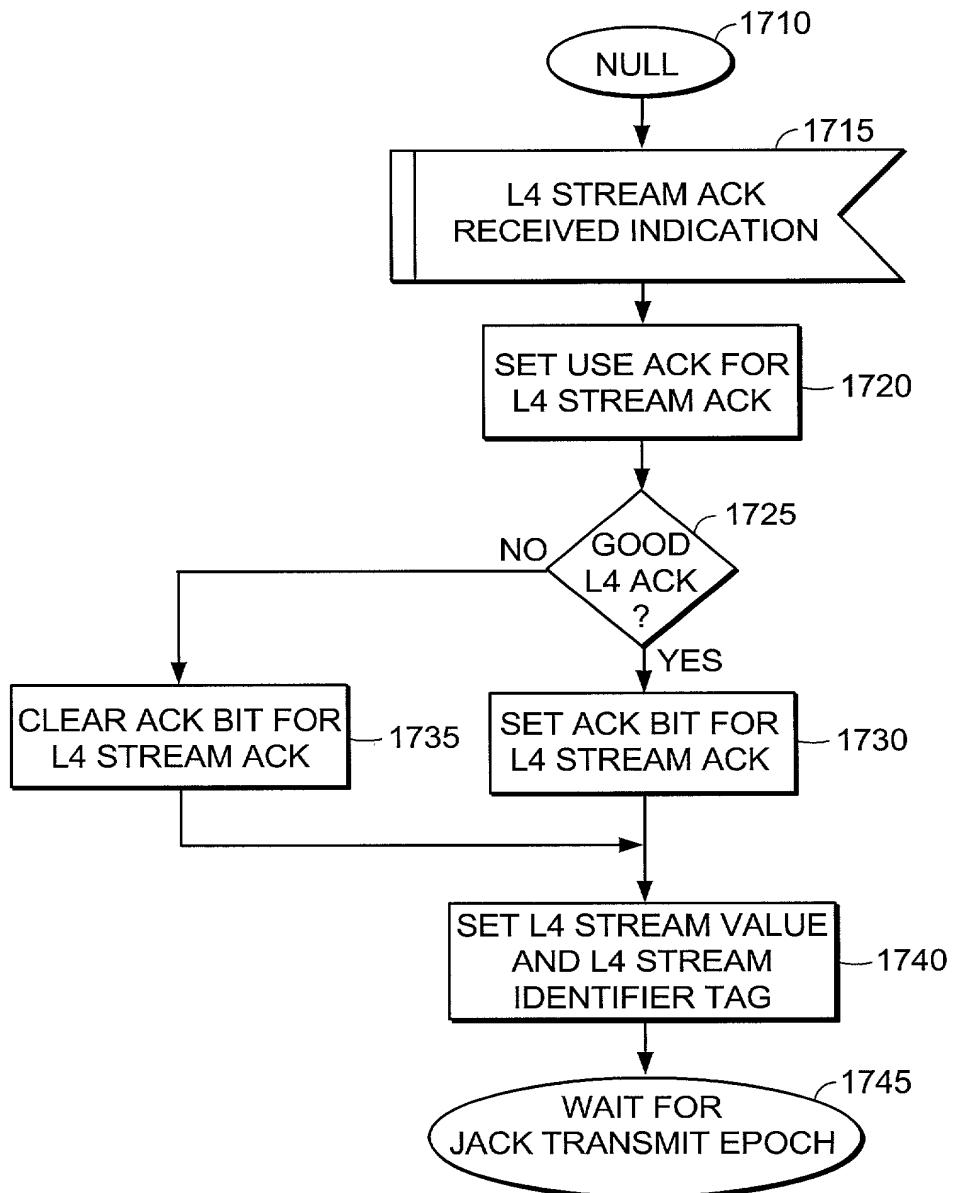


FIG. 17

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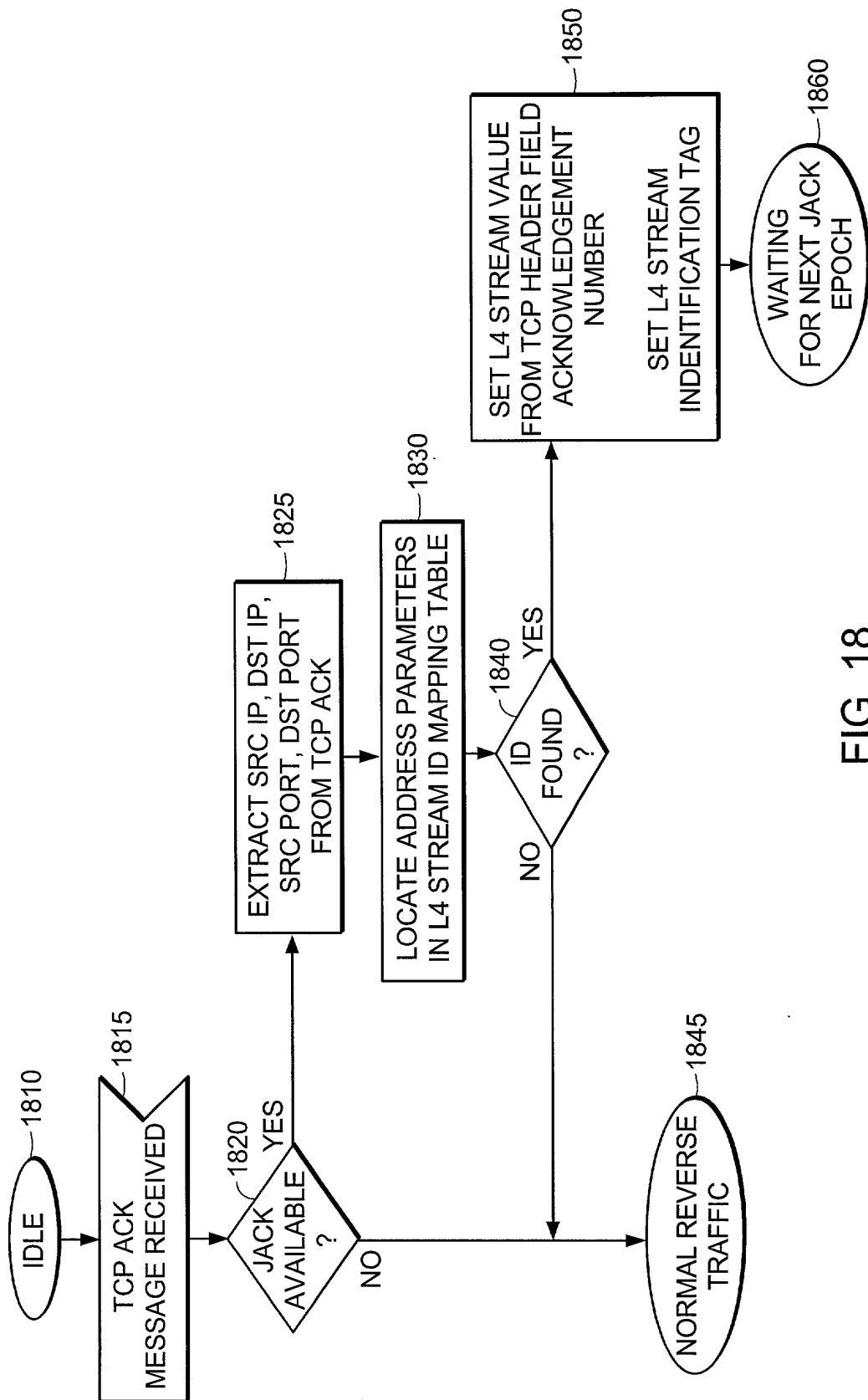


FIG. 18

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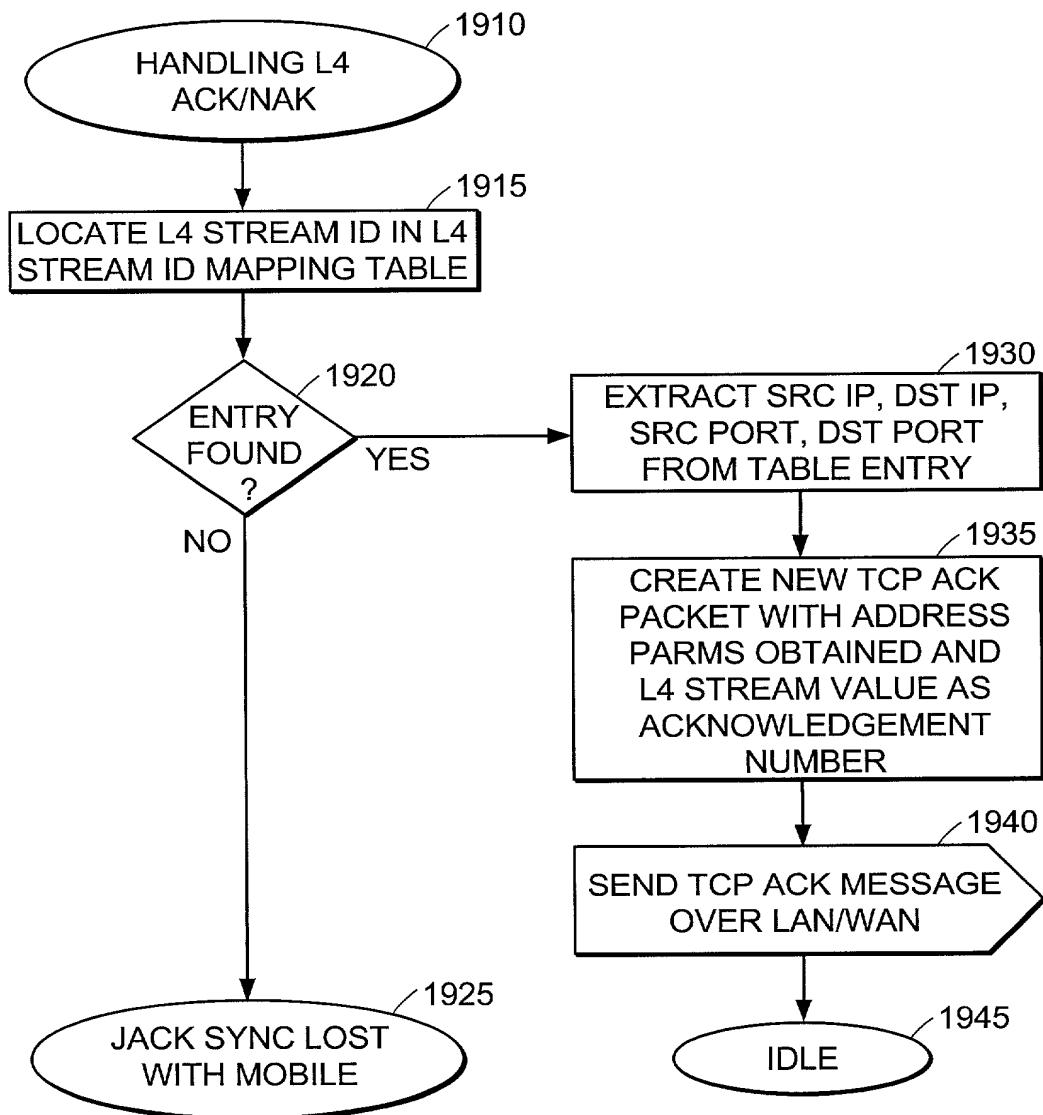


FIG. 19